U.S. Department of Commerce, Patent and Trademark Office	Attorney Docket No.:	004-7047
	Application No.:	10/050,358
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s):	Nicolai Kosche et al.
(Use several sheets if necessary)	Filing Date:	January 16, 2002
	Group Art Unit:	2183
	Date Submitted:	February 7, 2003

OIPE JC54 FEB 14 2003 PATENT & TRADEMARK OFFICE

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name
	AA			
	AB			
	AC			
	AD			
	AE			
	AF			
	AG			
	AH			
	AI			
	AJ			
	AK			

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Foreign Patent Documents

					Translation	
		Document	Date	Country	Yes	No
	AL					
	AM					
	AN					
	AP					

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AR	J. Anderson et al., "Transparent, Low-Overhead Profiling on Modern Processors," *Proceedings of the Workshop on Profile and Feedback-Directed Compilation* in conjunction with International Conf. on Parallel Architectures and Compilation Techniques (PACT98), Paris, France, Oct. 13, 1998, 5 pp.
	AS	"Compaq (Digital) Continuous Profiling Infrastructure (DCPI)" 1999, [online] [Retrieved from the Internet Dec. 20, 2002] URL <http://www.tru64unix.compaq.com/dcpi> and <http://www.research.compaq.com/SRC/dcpi> , 11 pp.
	AT	

Examiner		Date Considered	2-14-2003

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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LIST OF PENDING U.S. PATENT APPLICATIONS

The following pending U.S. patent applications are listed for consideration by the Examiner.

1) U.S. Pat. App. No. 09/996,088, filed Nov. 28, 2001, entitled "Aggressive Prefetch of Address Chains," naming inventors Peter Damron and Nicolai Kosche, 40 pp.

2) U.S. Pat. App. No. 10/050,387, filed Jan. 16, 2002, entitled "Technique for Associating Execution Characteristics with Instructions or Operations of Program Code," naming inventors Nicolai Kosche et al., 34 pp.

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Applicant(s) are providing a separate list of pending applications, pursuant to MPEP § 609(D), to avoid publication of the application numbers on any patent that issues from the above-identified application. Applicant(s) request that the Examiner indicate consideration of the pending applications listed above in accordance with MPEP 609(C)(2) and return a copy of this page with the next communication.

U.S. Department of Commerce, Patent and Trademark Office	Attorney Docket No.: 004-7047
	Application No.: 10/050,358
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s): Nicolai Kosche et al.
(Use several sheets if necessary)	Filing Date: January 16, 2002
	Group Art Unit: 2183
	Date Submitted: April 10, 2003

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U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name
MV	1.	6,427,235	July 30, 2002	Kosche et al.

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Foreign Patent Documents

					Translation	
		Document	Date	Country	Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner MV	Date Considered 4-14-2003

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Attorney Docket No.: 004-7047
Application No.: 10/050,358
Applicant(s): Nicolai Kosche et al.
Filing Date: January 16, 2002
Group Art Unit: 2183
Date Submitted: April 10, 2003

LIST OF PENDING U.S. PATENT APPLICATIONS

The following pending U.S. patent applications are listed for consideration by the Examiner.

1) U.S. Patent Application 09/436,464 filed November 8, 1999, entitled "A Method and Apparatus for Inserting Data Prefetch Operations Using Data Flow Analysis," naming inventor Peter C. Damron (Atty Ref. P4373).

2) U.S. Patent Application 09/535,930 filed March 24, 2000, entitled "Modulo Scheduling Via Binary Search for Minimum Acceptable Initiation Interval Method and Apparatus," naming inventor Brian E. Bliss (Atty Ref. P4678).

3) U.S. Patent Application 09/594,430 filed June 15, 2000, entitled "Compiler-Based Cache Line Optimization," naming inventor Nicolai Kosche (Atty Ref. P5010).

4) U.S. Patent Application 09/630,052 filed August 1, 2000, entitled "Method and Apparatus for Software Prefetching Using Non-Faulting Loads," naming inventors Peter Damron and Nicolai Kosche (Atty Ref. P4955).

5) U.S. Patent Application 09/679,431 filed October 3, 2000, entitled "System and Method for Scheduling Memory Instructions to Provide Adequate Prefetch Latency," naming inventors Nicolai Kosche, Peter C. Damron, Joseph Chamdani and Partha Tirumalai (Atty Ref. P5370).

6) U.S. Patent Application 09/679,433 filed October 3, 2000, entitled "System and Method for Insertion of Prefetch Instructions by a Compiler," naming inventors Peter C. Damron and Nicolai Kosche (Atty Ref. P5392).

7) U.S. Patent Application 09/679,434 filed October 3, 2000, entitled "System and Method for Scheduling Instructions to Maximize Outstanding Prefetches and Loads," naming inventors Peter C. Damron and Nicolai Kosche (Atty Ref. P5369).

8) U.S. Patent Application 09/685,431 filed October 10, 2000, entitled "Heuristic for Identifying Loads Guaranteed to Hit in Processor Cache," naming inventors Nicolai Kosche and Peter C. Damron (Atty Ref. P5371).

4-14-2003

Applicant(s) are providing a separate list of pending applications, pursuant to MPEP § 609(D), to avoid publication of the application numbers on any patent that issues from the above-identified application. Applicant(s) request that the Examiner indicate consideration of the pending applications listed above in accordance with MPEP 609(C)(2) and return a copy of this page with the next communication.

PTO/SB/08B (Substitute for form 1449/PTO)	Attorney Docket No.:	004-7047
	Application No.:	10/050,358
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor:	Nicolai Kosche
(Continuation Sheet)	Filing Date:	January 16, 2002
	Group Art Unit:	2192
	Examiner Name:	Rongfa Philip Wang
Sheet 2 of 2	Date Submitted:	February 7, 2006

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NON PATENT LITERATURE DOCUMENTS

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	AQ	LEBECK, Alvin R., "Cache Conscious Programming in Undergraduate Computer Science," *ACM SIGCSE Technical Symposium on Computer Science Education (SIGCSE '99), Proceedings of the 30th SIGSCE Tech Symposium on Comp. Sci. Edu.*, New Orleans, LA, USA, March 24-28, 1999, 5 pages.	
	AR	LIPASTI, Mikko H. et al., "SPAID: Software Prefetching in Pointer- and Call-Intensive Environments," *Micro 28 Proceedings of the 28th Annual International Symposium on Microarchitecture*; Nov. 29-Dec. 1, 1995, Ann Arbor, MI, 6 pages.	
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	AT	MARTONOSI, Margaret et al., "MemSpy: Analyzing Memory System Bottlenecks in Programs," *Proceedings of the1992 ACM SIGMETRICS Joint Int'l. Conf. on Measurement and Modeling of Computer Systems*, Newport, RI, USA, June 1-5, 1992, pages 1-12.	
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	AW	SPRUNT, Brinkley, "The Performance Monitoring Features of the Pentium 4 Processor," *IEEE Micro*, Vol. 22, No. 4, July 2002, pages 32-42.	
	AX	SRIDHARAN, K., "VTune: Intel's Visual Tuning Environment," Proceedings of USENIX-NT Windows NT Workshop, August 11, 1997, Seattle, CA, USA, retrieved from the Internet at URL http://www.usenix.org/publications/library/proceedings/usenix-nt97/presentations/sridhar.html, 24 pages.	
	AY	"Program Performance Analysis Tools," Sun Microsystems, Inc. Publication 817-0922-10, May 2003, retrieved from the Internet at URL http://docs.sun.com/source/817-0922/, 244 pages.	
	AZ	VENNERS, Bill, "The HotSpot Virtual Machine, How HotSpot Can Improve Java Program Performance and Designs," *Artima Developer*, May 1998, 7 pages, retrieved from the Internet at URL http://www.artima.com.	
	BA	WALKER, Kip et al., "Using Interpretation for Profiling the Alpha 21264a," presented at Carnegie Mellon University, October 1999, 29 pages.	
	BB	"The Java HotSpot Performance Engine Architecture," *Digital Forefront Magazine*, Winter 1997, pages 27-28, retrieved from the Internet at URL http://java.sun.com/products/whitepaper.html.	

Examiner Signature		Date Considered	2-10-2006

PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: *004-7047*
	Application No.: *10/050,358*
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor: *Nicolai Kosche*
	Filing Date: *January 16, 2002*
	Group Art Unit: *2192*
	Examiner Name: *Rongfa Philip Wang*
Sheet 1 of 1	Date Submitted: *February 7, 2006*

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] *(if known)*	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	US-5,526,499	06-11-1996	Bernstein et al.	
	AB	US-5,948,095	09-97-1999	Arora et al.	
	AC	US-6,000,044	12-07-1999	Chrysos et al.	
	AD	US-6,092,180	07-18-2000	Anderson et al.	
	AE	US-6,098,166	08-01-2000	Liebholz et al.	
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	AH	US-6,625,660	09-23-2003	Guthrie et al.	
	AI	US-6,675,374	01-06-2004	Pieper et al.	
	AJ	US-6,675,380	01-06-2004	McKinsey et al.	
	AK	US-6,681,317	01-20-2004	Matthews	
	AL	US-6,697,932	02-24-2004	Yoaz et al.	
	AM	US-6,748,589	06-08-2004	Johnson et al.	
	AN	US-6,880,073	04-12-2005	Arimilli et al.	
	AO	US-2002/0056078	05-09-2002	Inagaki	

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Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3]-Number[4]-Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
Examiner Signature				Date Considered	2-13-2006	

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[1]Applicant's unique citation designation number (optional).

[2]See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

[3]Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3)

[4]For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

[5]Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

[6]Applicant is to place a check mark here if English language Translation is attached.

PTO/SB/08B (Substitute for form 1449/PTO)	Attorney Docket No.:	004-7047
	Application No.:	10/050,358
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor:	Nicolai Kosche
	Filing Date:	January 16, 2002
	Group Art Unit:	2192
	Examiner Name:	Rongfa Philip Wang
Sheet 1 of 2	Date Submitted:	February 7, 2006

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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
	AA	ALPERN, B. et al., "The Jalapeno Virtual Machine," *IBM Systems Journal*, Vol. 39, No. 1, 2000, pages 211-238.	
	AB	ANDERSON, Jennifer et al., "Continuous Profiling (It's 10:43; Do You Know Where Your Cycles Are?)," abstract for presentation at HOT Chips 9, Stanford, CA, August 1997, 2 pages, retrieved from the Internet at URL http://h30097.www3.hp.com/dcpi/publications.htm.	
	AC	ANDERSON, Jennifer et al., "Instruction-Level Profiling on In-Order and Out-of-Order Processors," DEC presentation by Jeff Dean at Duke University, December 3, 1997, 42 pages, retrieved from the Internet at URL http://h30097.www3.hp.com/dcpi/publications.htm.	
	AD	ANDERSON, Jennifer M. et al., "Continuous Profiling: Where Have All the Cycles Gone?" *16th ACM Symposium on Operating Systems Principles*, St. Malo, France, October 5-8, 1997, 20 pages.	
	AE	ANDERSON, Jennifer et al., "Continuous Profiling: An Electron Microscope for Your Computer," presentation by Mark Vandevoorde to Silicon Valley summer interns, Palo Alto, CA, August 13, 1999, 18 pages.	
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	AG	BERRENDORF, Rudolf and ZIEGLER, Heinz, "PCL - The Performance Counter Library: A Common Interface to Access Hardware Performance Counters on Microprocessors (Version 1.2)," Technical Report FZJ-ZAM-IB-9816, Julich, Germany, October 1998, pages 1-50.	
	AH	BERC, Lance et al., "DIGITAL Continuous Profiling Infrastructure," DIGITAL Continuous Profiling Infrastructure Project, OSDI '96 Work-in-Progress Presentation, 2 pages, retrieved from the Internet at URL http://h30097.www3.hp.com/dcpi/publications.htm.	
	AI	BERC, Lance and VANDEVOORDE, Mark, "Hardware Support for Out-of-Order Instruction Profiling on Alpha 21264a," HotChips 11, Stanford, CA, USA, August 17, 1999, 21 pages.	
	AJ	BROWNE, Shirley et al., "A Scalable Cross-Platform Infrastructure for Application Performance Tuning Using Hardware Counters," *Proceedings of SC2000*, Dallas, TX, November 2000, 10 pages.	
	AK	BURROWS, Mike et al., "Efficient and Flexible Value Sampling," *Architectural Support for Programming Languages and Operating Systems (ASPLOS-IX)*, Nov. 1--3 2000, pages 1-19.	
	AL	CHILIMBI, Trishul M. et al., "Cache-Conscious Structure Layout," *Proceedings of the ACM SIGPLAN '99 Conf. on Programming Language Design and Implementation*, May 1999, 12 pages.	
	AM	CHILIMBI, Trishul J., et al., "Cache-Conscious Structure Definition," *Proceedings of the ACM SIGPLAN '99 Conf. on Programming Language Design and Implementation*, May 1999, 12 pages.	
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	AO	DEROSE, Luiz et al., "SIGMA: A Simulator Infrastructure to Guide Memory Analysis," *Conf. on High Performance Networking and Computing Proceedings of the 2002 ACM/IEEE Conf. on Supercomputing*, Baltimore, MD, 2002, 13 pages.	
	AP	ITZKOWITZ, Marty et al., "Memory Profiling Using Hardware Counters," in *Conf. on High Performance Networking and Computing Proceedings of the 2003 ACM/IEEE Conf. on Supercomputing*, Phoenix, AZ, November 15-21, 2003, pages 1-12.	

Examiner Signature		Date Considered	2-10-2006

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